## APPLICATION DATA SHEET

Applicants:

1<sup>st</sup> Inventor:

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Application Information:

Title: METHOD AND APPARATUS FOR PACKAGING TEST

INTEGRATED CIRCUITS

Classification:

Class:

Subclass:

Tech Center:

Drawing Sheets: 7

Drawing to be Published:

Docket No:

H0947

Type: Secrecy: Utility None

Representative Information: Kelly K. Kordzik, Reg. No. 36,571

Barry S. Newberger, Reg. No. 41,527 Robert A. Voigt, Jr., Reg. No. 47,159 Richard F. Frankeny, Reg. No. 47,573

**Domestic Priority:** 

Domestic Priority:		D (A - linetions	Parent Filing Date:
Application:	Continuity Type:	Parent Application:	Talent Fining Dute.
None			

**Foreign Priority Information** 

Foreign Priority Information		Till Dates	Priority Claimed:	
Country:	<b>Application Number:</b>	Filing Date:	Thority Claimed.	
None				

Assignee:

Advanced Micro Devices, Inc.

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